The processor as recited in claim \mathcal{I} , wherein said accumulator is a register having a width of 192 bits .--

Remarks

Upon entry of the foregoing amendment, claims 41-78 are pending in the application. This amendment seeks to cancel claim 1 and add new claims 41-78. These changes are believed to introduce no new matter, and their entry is respectfully requested.

The Examiner has rejected claim 1 under 35 U.S.C. § 101 for double patenting in view of U.S. Patent No. 5,864,703. Applicants have canceled claim 1. Thus, this rejection is now moot. By the foregoing, Applicants seek to add new claims 41-78. Favorable consideration and allowance of these new claims is respectfully solicited.

The Examiner is invited to telephone the undersigned representative if he believes that an interview might be useful for any reason.

Respectfully submitted,

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